

OIPE
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 PATENT & TRADEMARK OFFICE

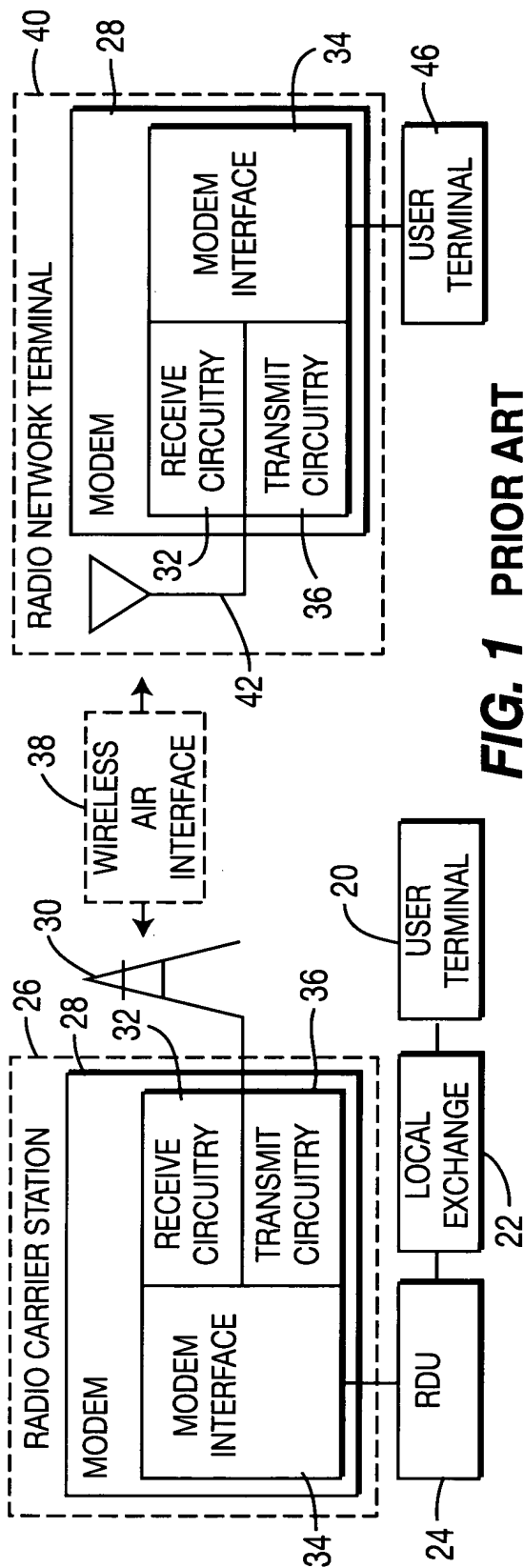


FIG. 1 PRIOR ART

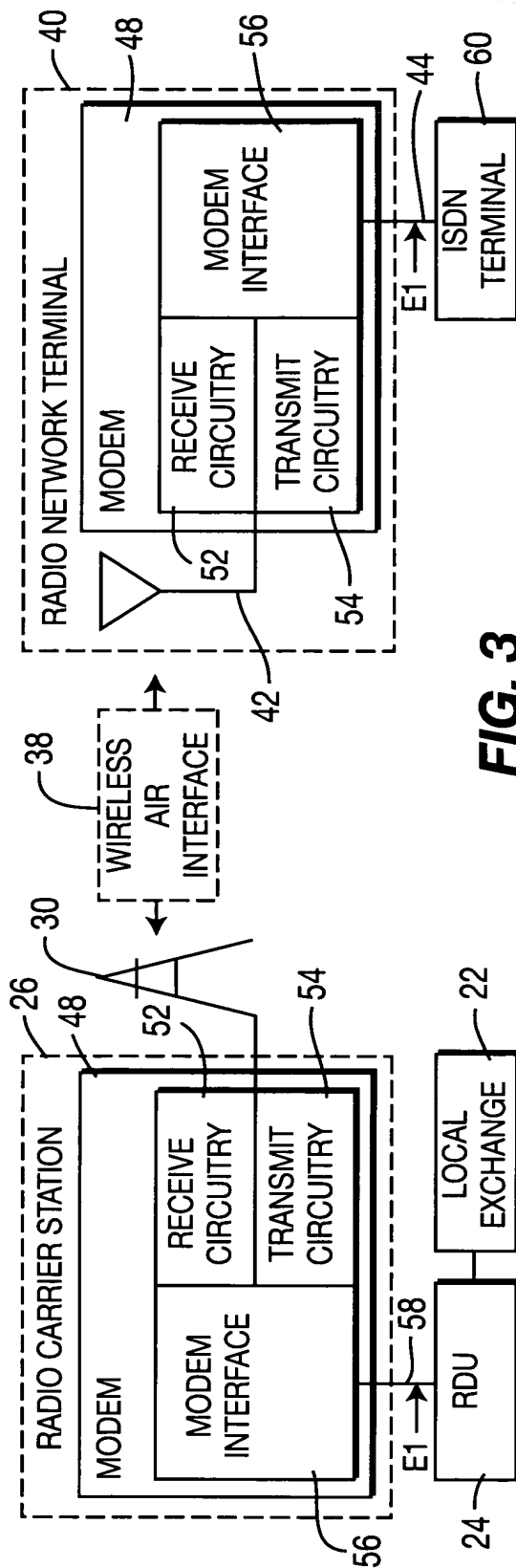


FIG. 3

FIG. 2B

[illegible]

FIG. 2A



MODEM INTERFACE-TSI BLOCK

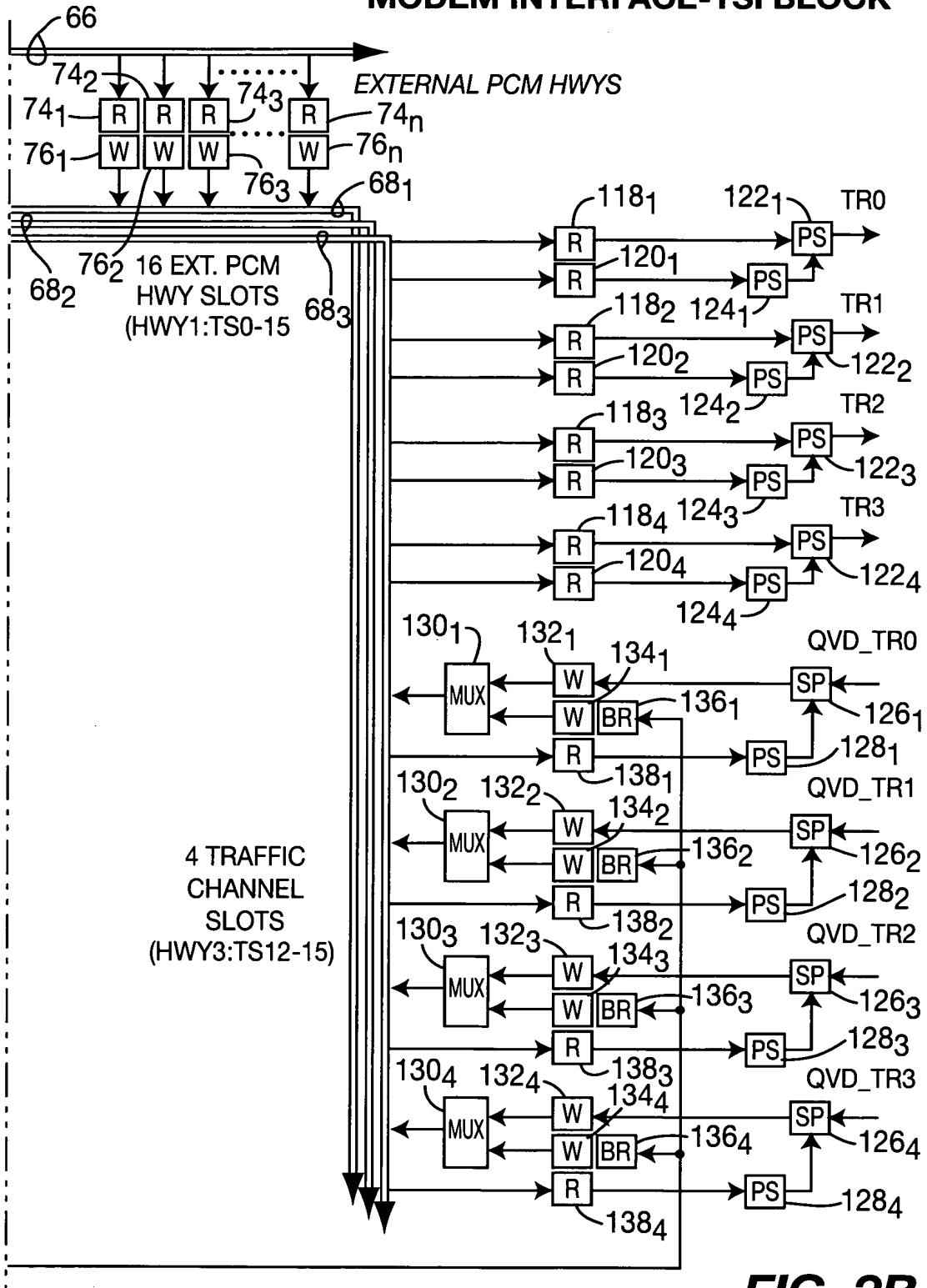


FIG. 2B